List of Reference Symbols

X1	First data word
X1'	Transformed first data word
X2	Second data word
X3	Third data word
VX	Comparison data word
S2	Sequence
I	Additional information
T1	First transformation
T2	Second transformation
T3	Third transformation
K	Comparison
MEM	Memory
CPU	Arithmetic and logic unit
DEC	First transformation device
R1	Second transformation device
R2	Third transformation device
COMP	Checking device
1	First buffer
2	Second buffer
3	Internal unit
ALARM	Alarm function